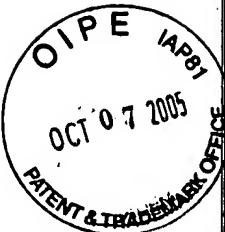


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/699,221 Confirmation No.: 2799
First Named Inventor: Constantin Bulucea Filing Date: 31 October 2003
Group Art Unit: 2814 Examiner: Farahani, D.
Atty. Docket No.: NS-5127-1D US
Title: Design and Operation of Gate-enhanced Junction Varactor With Gradual Capacitance Variation
Assignee: National Semiconductor Corporation

Mountain View, California
7 October 2005

COMMISSIONER FOR PATENTS
PO Box 1450
Alexandria, Virginia 22313-1450

RESPONSE TO RESTRICTION REQUIREMENT

Sir:

Responsive to the Restriction Requirement mailed 9 September 2005 for the above patent application, Applicant's Attorney provisionally elects "species" I for prosecution in this application.

Claims Readable on Elected "Species"

"Species" I is described in the Restriction Requirement as "A method comprising selecting the varactor of figures 8a, 8b, 8c, 8d, 10a, 10b, 10c, 10d, and 17, wherein the varactor has a an [sic] electrode 116 located opposite to the gate electrode, in those figures".

The pending claims are all method claims. None of the drawings illustrates the complete method of any of the pending claims. To the extent that a requirement to restrict a U.S. patent application to part of a set of method claims can be formulated in terms of species directed to certain of the application's drawings when none of those drawings illustrates the method of any of the pending claims, Claims 61 - 67, 73, 74, 76, 79 - 82, 107 -

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